FIG.1

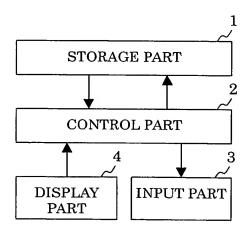


FIG.2

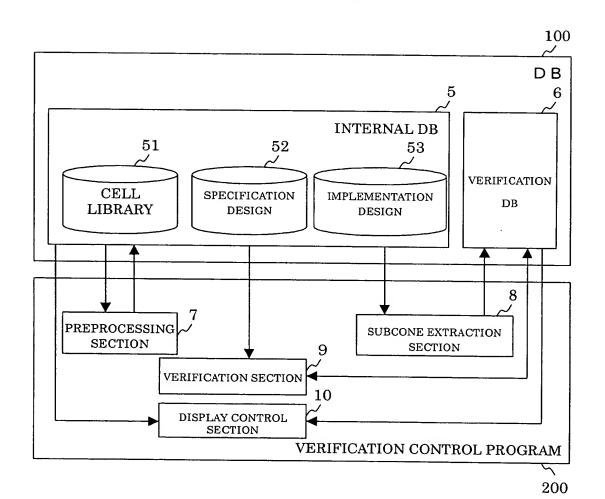


FIG.3

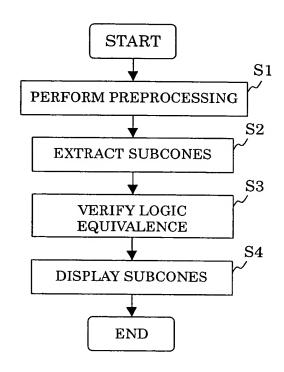


FIG.4A

FIG.4B

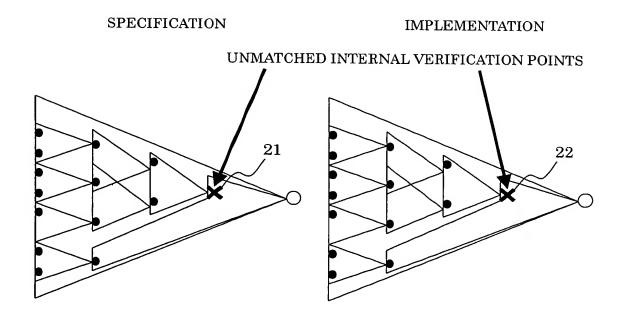


FIG.5

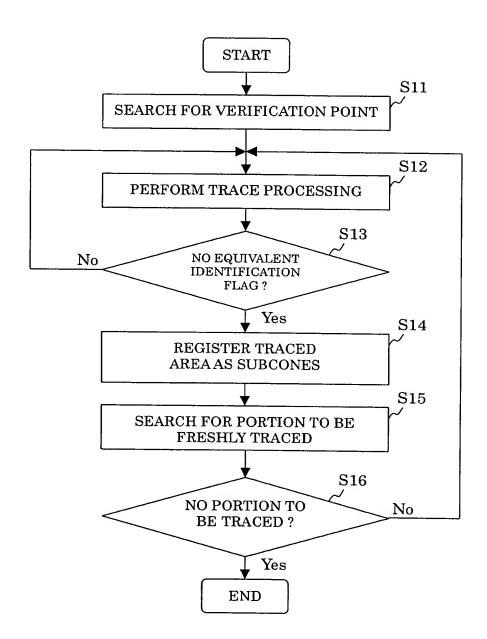
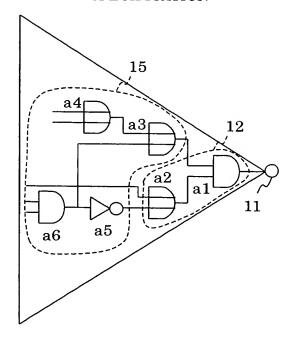


FIG.6A

FIG.6B

SPECIFICATION



IMPLEMENTATION

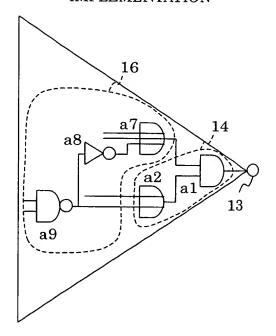


FIG.7A

SPECIFICATION

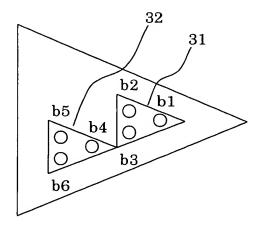


FIG.7B

IMPLEMENTATION

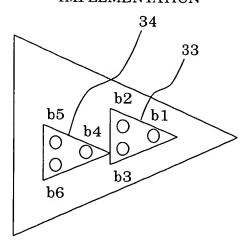


FIG.8

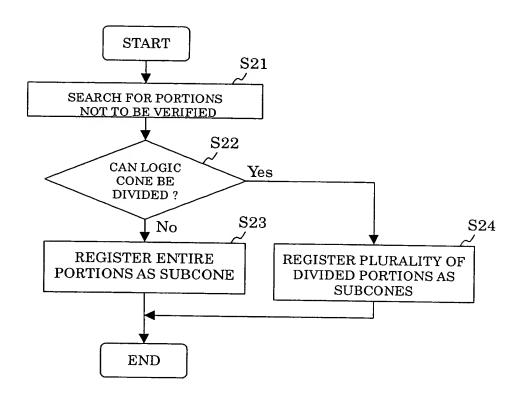


FIG.9A

FIG.9B

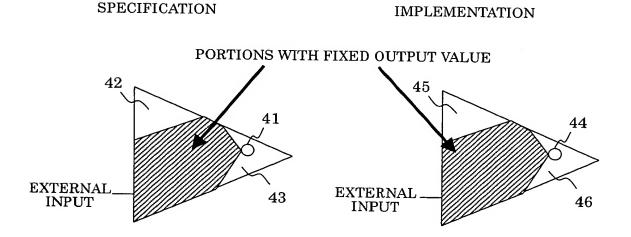
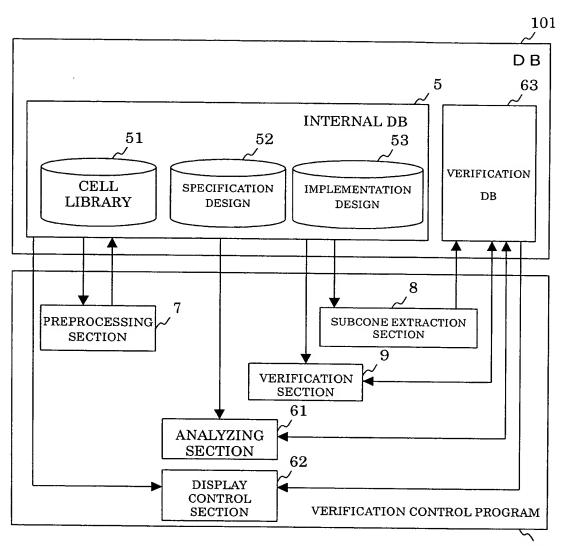


FIG. 10



201

FIG.11

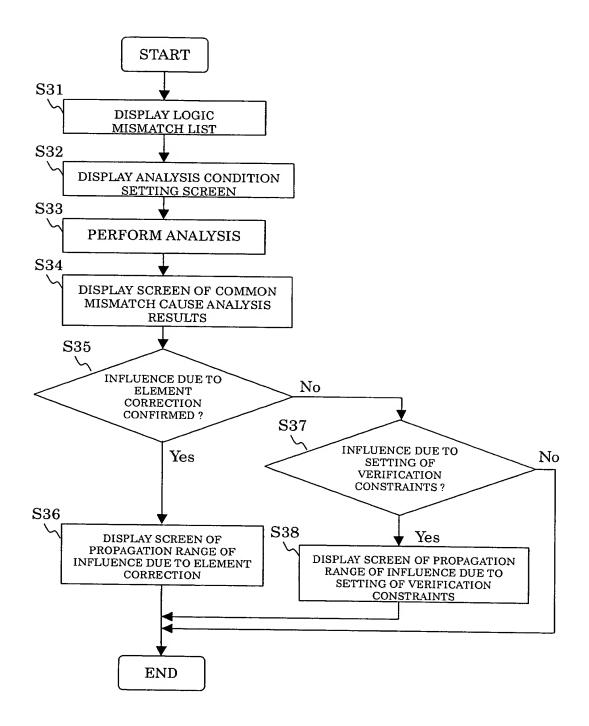


FIG.12

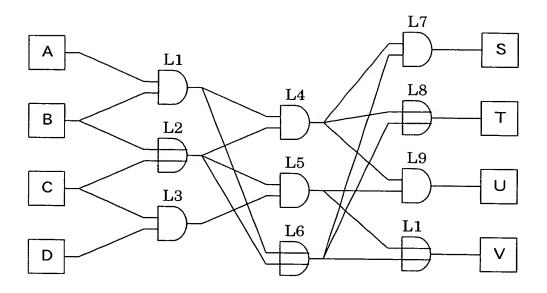
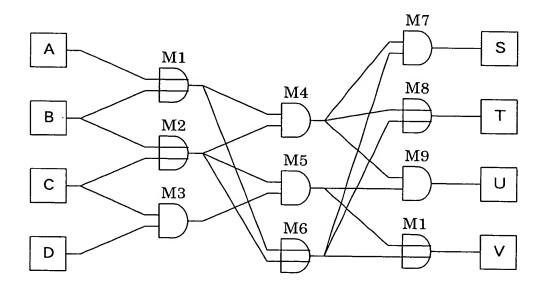


FIG.13



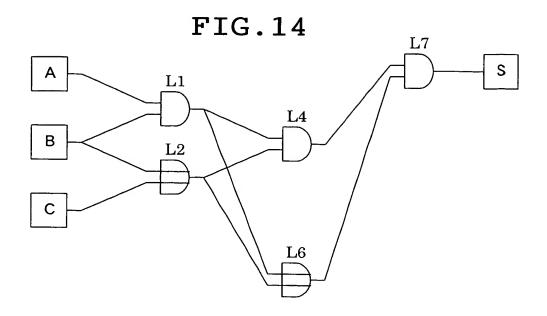


FIG.15

	LOGIC M	ISMATCH LIST	
1	SPECIFICATION	S	
l l	IMPLEMENTATION	S	
2	SPECIFICATION	Т	
	IMPLEMENTATION	Т	
3	SPECIFICATION	U	
3	IMPLEMENTATION	U	
4	SPECIFICATION	V	
4	IMPLEMENTATION	V	
5	SPECIFICATION		
	IMPLEMENTATION		1411
6	SPECIFICATION		
	IMPLEMENTATION		
	ANA	ALYSIS CONDITION SETTIN	VG

FIG.16

ANALY	SIS CONDI	TIO	N SETTING		
MENT KIND	S				
■ ON	□OFF	$A\Gamma$	DER	\square O N	■ OFF
O N	\Box OFF	3S	TATE	\square O N	■ OFF
\square ON	■ OFF	WI	RED LOGIC	\square O N	■ OFF
\square ON	■ OFF	IN	VERTER	□ON	M OFF
□ON	■ OFF	BU	FFER	□on	■OFF
□ON	O FF				
и О 🗆 иог	■ OFF				
□ON	■ OFF				
MENT CON	VECTION S	TAT	US		
UT NUMBE	R		2]	
UT NUMBE	R		NO LIMIT		
PUT BRAN	CH NUMBI	ER	NO LIMIT		
ΓΡUT BRAN	ICH NUMB	ER	NO LIMIT		
		•			
CURRENCI	ES				
R MORE TH	AN 1]			70
		J			72
			ANALYS	IS EXEC	UTION
	MENT KIND ON ON ON ON ON ON ON ON ON TON ON TON T	MENT KINDS ON OFF UND ON OFF UND ON OFF UND ON OFF	MENT KINDS ON OFF AD ON OFF AD ON OFF WI ON OFF IN ON OFF BU ON OFF ON OFF ON OFF TON OFF TON OFF TON OFF TON TOFF TON TOFF MENT CONNECTION STATE UT NUMBER THE BRANCH NUMBER THE BRANCH NUMBER CURRENCES	■ON □OFF ADDER ■ON □OFF 3STATE □ON ■OFF WIRED LOGIC □ON ■OFF INVERTER □ON ■OFF □ON ■OFF □ON ■OFF □ON ■OFF □ON ■OFF □UT NUMBER □UT NUMBER □UT NUMBER □UT BRANCH NUMBER	MENT KINDS ON OFF ADDER ON ON OFF STATE ON ON OFF WIRED LOGIC ON ON OFF INVERTER ON ON OFF BUFFER ON ON OFF ON OFF ON OFF UNDON OFF ON OFF ON OFF ON OFF ON OFF CONNECTION STATUS UT NUMBER OUT NUMBER OUT NUMBER OUT BRANCH NUMBER

FIG.17

						···•	\Rightarrow
SPEC	EIFICATIONS	S	Т	U	V		APPLICABLI NUMBER
\bigcap	L 1						4
	L 2						4
	L 3						2
	L 4						3
	L 5						2
	L 6						3
	L 7						1
	L 8						1
	L 9						1
₩	L10						
LJ				<u> </u>			1
J							
IMPL	EMENTATION	\$	Т	U	V		
IMPLI		S	T	U	V		APPLICABLE
IMPLI	EMENTATION	\$ •	<u>-</u>	U			APPLICABLE NUMBER
IMPLI	EMENTATION M 1			U			APPLICABLE NUMBER
IMPLI	EMENTATION M 1 M 2						APPLICABLE NUMBER 4 4
IMPLI	M 1 M 2 M 3						APPLICABLE NUMBER 4 4 2
IMPLI	M 1 M 2 M 3 M 4						APPLICABLE NUMBER 4 4 2 3
IMPLI	M 1 M 2 M 3 M 4 M 5						APPLICABLE NUMBER 4 4 2 3 2
IMPLI	M 1 M 2 M 3 M 4 M 5 M 6						APPLICABLE NUMBER 4 4 2 3 2 3 2 3
IMPLI	M 1 M 2 M 3 M 4 M 5 M 6 M 7						APPLICABLE NUMBER 4 4 2 3 2 3 1

FIG. 18

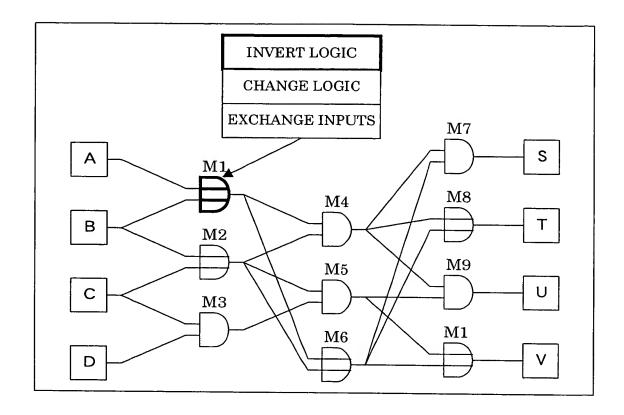


FIG.19

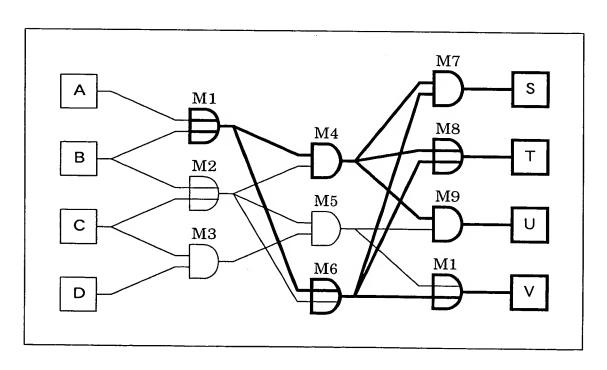


FIG.20

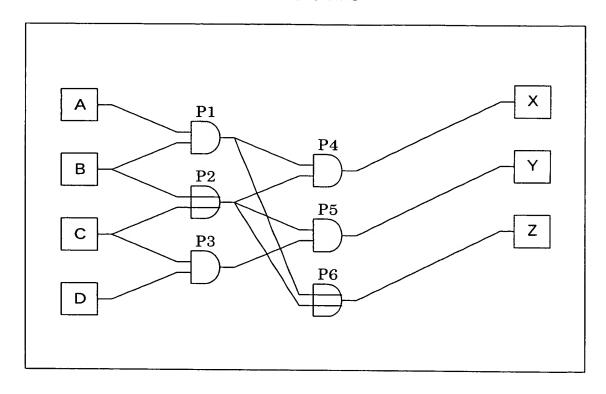


FIG.21

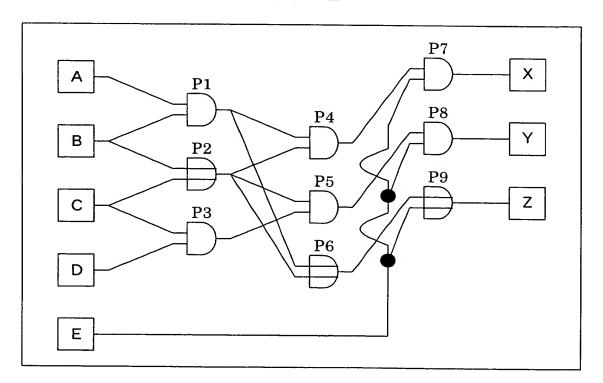


FIG. 22

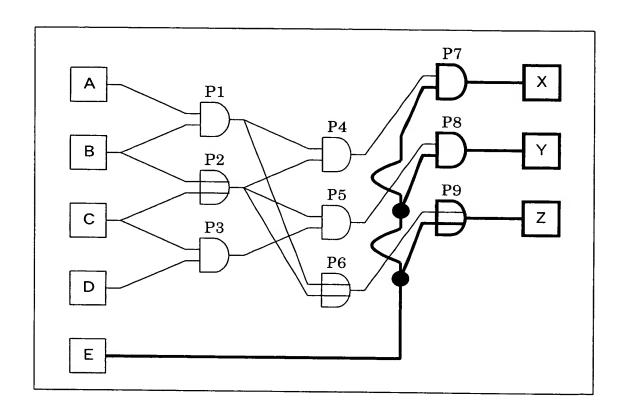


FIG.23

